



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Art Unit: 2812

Title: **METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING
STACKED-GATE STRUCTURE**

Date: **April 22, 2005**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT COVER SHEET

Sir:

Attached hereto please find the following:

1. An Amendment filed in response to the Office Action mailed on **February 8, 2005**;
2. Information Disclosure Statement;
3. Form PTO-1449;
4. Foreign-language reference;
5. Foreign-language office action; and
6. Return receipt postcard.

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